Döcket No.: 67161-079 PATENT

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of : Customer Number: 20277

Tetsushi TANIZAKI : Confirmation Number:

Serial No.: : Group Art Unit:

Filed: August 28, 2003 : Examiner:

For: SEMICONDUCTOR MEMORY DEVICE PERMITTING BOUNDARY SCAN TEST

## INFORMATION DISCLOSURE STATEMENT

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Dear Sir:

In accordance with the provisions of 37 C.F.R. 1.56, 1.97 and 1.98, the attention of the Patent and Trademark Office is hereby directed to the documents listed on the attached form PTO-1449. It is respectfully requested that the documents be expressly considered during the prosecution of this application, and that the documents be made of record therein and appear among the "References Cited" on any patent to issue therefrom.

This Information Disclosure Statement is being filed within three months of the U.S. filing date OR before the mailing date of a first Office Action on the merits. No certification or fee is required.

The relevance of the Japanese Patent Laying-Open No. 2001-243798, is discussed in the present specification.

## Serial No.:

Please charge any shortage in fees due in connection with the filing of this paper, including extension of time fees, to Deposit Account 500417 and please credit any excess fees to such deposit account.

Respectfully submitted,

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INFORMATION DISCLOSURE CITATION IN AN APPLICATION  (PTO-1449)  LS. PATENT DOCUMENTS  EXAMINER'S INITIALS INS US	
Tetsushi TANIZAKI	
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Yes	No
JP2003-196983A 07/11/2003 MITSUBISHI DENKI JAPAN (w/ KABUSHIKI KAISHA English Abstract)	
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